Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	5525	(intrq inrq interrupt) adj (line bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:09
L2	1684	(ack acknowledge acknowledgment) adj (line bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:09
L3	111831	(cpu processor) same (Pld logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:33
L4	293	1 and 2 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 14:07
L5	270	4 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:13
L6	86	1 same 2	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:32
L9	8	3 same 6	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:13
L10	8	9 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:17
<b>L11</b>	8	1 same 2 same 3	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:16
L13	26473	(cpu processor) and (Pld (programmable adj2 logic))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:18
L14	15	1 same 2 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:17

L15	14	14 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:19
L16	56	(cpu processor) same 6	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:18
L17	48	16 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:33
L18	0	6 and (connection adj2 state adj2 connector)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:32
L19	314	(connection adj2 state adj2 connector)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:32
L20	16	(cpu processor) same 19	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:33
L21	12	20 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:49
L22	4	1 and 2 and (connection adj state)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/01/31 12:49
			; IBM_TDB			
L23	0	22 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 12:51
Ľ24	0	1 and 2 and (connection adj status)	US-PGPUB;	OR	ON	2006/01/31 12:49
			USPAT; EPO; JPO;			
			DERWENT; IBM_TDB			
L25	75	6 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:24

L26	51	ack same irq same (processor cpu controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:25
L27	38	26 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:28
L28	4	ack same irq same (control adj (line bus))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:27
L29	27	ack same irq same ((ack irq control) adj2 (line bus))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:33
L30	20	29 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:33
L31	24	ack same irq same ((ack irq) adj2 (line bus))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:33
L32	0	24 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:33
L33	57	ack same irq same (line bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:37
L34	41	33 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:38
L35	1	ack same irq same (line bus) same (sense sensing detect\$4) same connector	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:38
L36	7	ack same irq same (line bus) same (sense sensing detect\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 14:00

			т			
L37	4	36 and @ad<="20000828"	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:42
L38	64123	(jEONG adj KWON adj lee).in. or (LG adj ELECTRONICS).as.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:42
L39	1	36 and 38	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:45
L40	1	ack same connector same (cpu processor controller) same chang\$4 same (state status) same connector	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:46
L41	0	ack same connector same (cpu processor controller) same chang\$4 same (state status) same adapter	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 13:46
L42	1	(ack same irq same (line bus) same (sense sensing detect\$4)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 14:00
L43	8	4 AND 709/250.CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2006/01/31 14:07



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Tue, 31 Jan 2006, 1:16:03 PM EST

#1	(((acknowledge <near>line <and> interrupt <near>line)<in>metadata)) <and> (pyr &gt;= 1950 <and> pyr &lt;= 1999)</and></and></in></near></and></near>
#2	((acknowledgement <near>line <and> interrupt <near>line)<in>metadata)</in></near></and></near>
#3	((acknowledgement <near>line <and> interrupt <near>line)<in>metadata)</in></near></and></near>
#4	((pld <paragraph> cpu <or> processor )<in>metadata)</in></or></paragraph>
<u>#5</u>	(((pld <paragraph> cpu and acknowledge<near> line <and> interrupt<near> line) <in>metadata)) <and> (pyr &gt;= 1950 <and> pyr &lt;= 1999)</and></and></in></near></and></near></paragraph>
<u>#6</u>	(((pld <paragraph> cpu )<in>metadata)) <and> (pyr &gt;= 1950 <and> pyr &lt;= 1999)</and></and></in></paragraph>
#Z	(((acknowledge <near> line <paragraph> interrupt<near>line)<in>metadata)) <and> (pyr &gt;= 1950 <and> pyr &lt;= 1999)</and></and></in></near></paragraph></near>



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